



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q63926

Koutaro HACHIYA

Appln. No.: 09/822,512

Group Art Unit: 2128

Confirmation No.: 7509

Examiner: Fred Ferris

Filed: April 2, 2001

For: METHOD AND APPARATUS FOR MATRIX REORDERING AND ELECTRONIC  
CIRCUIT SIMULATION

**STATEMENT OF SUBSTANCE OF INTERVIEW**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please review and enter the following remarks summarizing the interview conducted on  
October 27, 2005:

**REMARKS**

An Examiner's Interview Summary Record (PTO-413) was attached with the Non-Final  
Office Action dated November 2, 2005.

During the interview, the following was discussed:

1. Identification of claims discussed: 1-16.
2. Results of Interview: An agreement was not reached between the Examiner and the  
Applicant's representative Mr. Chid S. Iyer. The Examiner called the Applicant and indicated  
that Examiner's SPE had suggested amending independent claims to recite a "determining means  
for..." (instead of a "determinator") and a replacing means for..." (instead of a "replacer") in

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order to resolve 112(1) issues. The Applicant's representative indicated that he would contact client in Japan regarding this issue but also indicated that this could take a while.

It is respectfully submitted that the instant STATEMENT OF SUBSTANCE OF INTERVIEW complies with the requirements of 37 C.F.R. §§1.2 and 1.133 and MPEP §713.04.

**It is believed that no petition or fee is required.** However, if the USPTO deems otherwise, Applicant hereby petitions for any extension of time which may be required to maintain the pendency of this case, and any required fee, except for the Issue Fee, for such extension is to be charged to Deposit Account No. 19-4880.

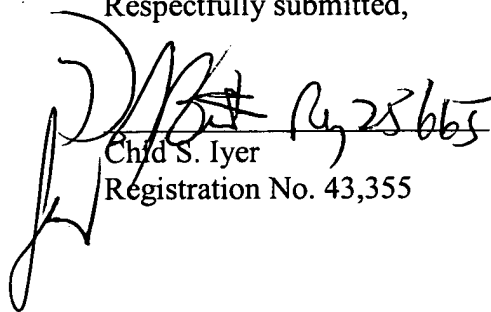
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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Respectfully submitted,



Chid S. Iyer  
Registration No. 43,355

Date: February 2, 2006